

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
17 February 2005 (17.02.2005)

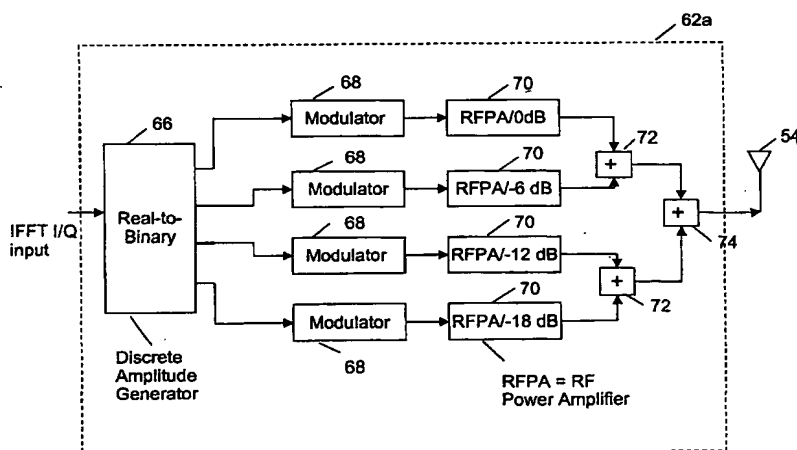
PCT

(10) International Publication Number  
**WO 2005/015732 A3**

- (51) International Patent Classification<sup>7</sup>: **H04L 27/00**, H04K 1/02
- (21) International Application Number: PCT/US2004/025397
- (22) International Filing Date: 6 August 2004 (06.08.2004)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data: 60/493,214 7 August 2003 (07.08.2003) US
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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:  
— with international search report

[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR DISCRETE POWER SYNTHESIS OF MULTICARRIER SIGNALS WITH CONSTANT ENVELOPE POWER AMPLIFIERS



(57) Abstract: A power synthesizer is in several ( $n$ ) discrete stages, each stage having a discrete amplitude, constant envelope amplifier coupled in series with a continuous phase modulator. Each modulator is actuated simultaneously, but not necessarily to modulate the signal phase similarly. For circuit power combining, the modulators are coupled to inputs of the amplifiers, while the separately modulated and amplified bits are combined prior to being fed to the antenna. For spatial power combining, each modulator is coupled to the input of an amplifier which in turn feeds one of several antennas. Each stage operates on one bit, and each successive  $n^{\text{th}}$  stage amplifies the input bit by a factor of  $2^{n-1}$ . Preferably, a discrete amplitude generator converts an (analog) output of an IFFT block to a binary sequence, and each bit is fed to a separate stage. The power synthesizer converts a digital output of the discrete amplitude generator to analog at RF rather than at baseband, rendering a baseband DAC, and a separate upconverter, unnecessary.



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

**(88) Date of publication of the international search report:**  
9 June 2005